

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hiroyuki TAKAHASHI et al.
Title: SEMICONDUCTOR MEMORY DEVICE AND
SEMICONDUCTOR MEMORY DEVICE CONTROL
METHOD
Appl. No.: 10/507,117
Filing Date: 9/10/2004
Examiner: Hur, Jung H.
Art Unit: 2824
Confirmation Number: 4725

AMENDMENT AND REPLY UNDER 37 CFR 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Non-Final Office Action dated August 22, 2006, concerning the above-referenced patent application.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this document.

Remarks/Arguments begin on page 21 of this document.

Please amend the application as follows: